

## ABSTRACT

Multiplying circuits 101 to 105, storage circuits  
5 111 to 115, first selection circuits 131 to 135, adding  
circuits 121 to 125 and second selection circuits 141  
to 145 arranged on 16-stage data paths constitute  
calculation circuits to execute correlation processing  
on respective data paths. 16-bit codes that are basic  
10 structures of a PSC and SSC generated in code generating  
circuit 170 as despreading codes are constant in  
repetition characteristics of positive bits and negative  
bits, and it is thus possible to execute the correlation  
processing on received data with a one-chip mutual shift  
15 in sixteen calculation circuits. It is thereby possible  
to execute the correlation processing at desired timings  
in the first step, second step and third step processing.